

IN THE UNITED STATES PATENT AND TRADE MARKS OFFICE

Applicant: Gurpreet BHULLAR et al
Serial No: unknown
Filed: on even date
Title: PROCESS, VOLTAGE, TEMPERATURE INDEPENDENT
SWITCHED DELAY COMPENSATION SCHEME
Art Unit: 2631
Examiner: Kevin Michael BURD
Our File: 0243P56US03

November 5, 2003

U.S. Patent and Trademark Office
2011 South Clark Place
Customer Window,
Crystal Plaza Two, Lobby, Room 1B03
Arlington, VA 22202

Dear Sir:

This application claims priority under 35 USC 120 from application 09/968,897 and 09/106,755.

Under the provisions of 37 C.F.R. 1.97, including particularly subsections (a) and (e) thereof, and 37 C.F.R. 1.98 including specifically subsection (a) thereof, Applicant calls to the attention of the Examiner the following documents:

United States Patent 3,829,843 - Cichetti Jr. et al.

United States Patent 5,295,164 - Yamamura

United States Patent 5,515,403 - Sloan et al.

United States Patent 5,544,203 - Casasanta et al.

United States Patent 5,604,775 - Saitoh et al.

United States Patent 5,963,069 - Jefferson et al.

United States Patent 5,969,551 - Fujioka

United States Patent 6,327,318 -Bhullar et al.

European Patent 0,704,975

Applicant respectfully requests that these references be entered into the record of this application. The Examiner is requested to initial the appropriate area on the enclosed copy of PTO/SB/08A, thereby indicating consideration by the Examiner of the references.

Applicant respectfully submits that the present claims are patentable over the references identified herein, and requests timely and favourable examination of this application.

Respectfully submitted,



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Enclosure